

1c979 U.S. PTO
09/777828
09/07/81

Class	Subclass	ISSUE CLASSIFICATION
341	22	

PATENT NUMBER

6680678

6680678

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

TR
SCANNED HCL O.A. JH

DAN 20 2004

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/777828		341	22	2846	Edwards

APPLICANTS
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Yin-Chen Huang

2635

TITLE

Circuit layout arrangement for bus switch signal recognition

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
341	22	341	26				
INTERNATIONAL CLASSIFICATION		400	477				
H03K	17/94	200	5A				
H03M	11/00						

☐ Continued on Issue Slip Inside File Jacket

12/11/03

Formal Drawings (5 sheets)

2/7/01

<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. <u>5</u> Figs. Drwg. <u>7</u> Print Fig. <u>6</u>			CLAIMS ALLOWED Total Claims <u>5</u> Print Claim for O.G. <u>1</u>	
	_____ (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED <u>9/30/03</u>	
	TIMOTHY EDWARDS PRIMARY EXAMINER <u>Aug 21 14</u> <u>9/21/03</u> (Primary Examiner) (Date)			ISSUE FEE <u>1W</u> Amount Due <u>\$300.00</u> Date Paid <u>11-25-03</u>	
	<u>MDS</u> <u>10-2-03</u> (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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ISSUE FEE IN FILE

(FACE)